



Application Serial No. 09/885,393  
Supplemental IDS

MI22-1694

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application Serial No. .... 09/885,393  
Filing Date .... June 19, 2001  
Inventor .... Richard Holscher et al.  
Assignee .... Micron Technology, Inc.  
Group Art Unit .... 1756  
Examiner .... Kathleen Duda  
Attorney's Docket No. .... MI22-1694  
Title: Semiconductor Processing Methods

To: Mail Stop RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

**Reference -- See Attached Form PTO-1449**

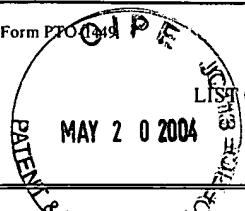
This Request for Continued Examination (RCE) Application is being filed in an abundance of caution to ensure consideration of the references listed on the attached form PTO-1449.

The attached form PTO-1449 is submitted in compliance with 37 CFR §1.56. No admission is made regarding whether the submitted reference is prior art. For the convenience of the Examiner we have enclosed a copy of the Japanese references and a copy of the article.

Respectfully submitted,

Dated: 5/20/04

By:   
Robert C. Hyta; Reg. No. 46,791

 <p>U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE</p> <p>LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)</p> <p>MAY 20 2004</p>		ATTY. DOCKET NO. MI22-1694		SERIAL NO. 09/885,393			
		APPLICANT Richard Holscher et al.					
		FILING DATE June 19, 2001		GROUP 1756			
		U.S. PATENT DOCUMENTS					
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	4,971,655	11/20/1990	Stefano et al.	156	659	
	AB	6,153,504	11/28/2000	Shields et al.	438	613	
	AC	6,087,064	07/11/00	Lin et al.	430	270.1	
	AD	5,986,318	11/16/99	Kim et al.	257	437	
	AE	6,498,084 B2	12/24/02	Bergemont	438	593	
	AF	5,036,383	07/30/91	Mori	357	71	
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation
							Yes
	AM	08-045926	16/02/1996	Japan (Abstract)			
	AN	10-163083	19/06/1998	Japan			x
	AO	JP8051058	20/02/96	Japan (Abstract)			
	AP	JP63316476	23/12/88	Japan (Abstract)			
	AQ	JP8078322	22/03/96	Japan (Abstract)			
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
	AR		Wolf, S., "Silicon Processing for the VLSI Era", Vol. 3; pg. 635				
	AS						
	AT						
EXAMINER				DATE CONSIDERED			
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>							